Abstracts

Digital Frequency Multipliers Using Multisection Two-Strip Coupled Line

I. Sakagami, N. Miki, N. Nagai and K. Hatori. "Digital Frequency Multipliers Using Multisection Two-Strip Coupled Line." 1981 Transactions on Microwave Theory and Techniques 29.2 (Feb. 1981 [T-MTT]): 118-122.

This paper describes new networks which acts as digital frequency multipliers such as doubler, tripler, and so on for input clock frequency. The networks consist of cascaded sections of uniform lossless commensurate coupled-transmission-lines and three resistors of II-structure, and the proposed multipliers are quite new in the sense of being built without using active or nonlinear circuit elements. The theoretical and experimental results for a coupled-line digital frequency doubler are compared and found to be in good agreement.

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